

FMC to MHVA4F Connections for DM & DAC Output (x = LPC or HPC) 2/5/19

Net names are from the Xilinx Zynq ZC706 development board. The net names on your board may differ.

The mapping of VITA FMC signal names and pins to DM signals is implemented on the MHVA4F board and is independent of the carrier board's net names.

DM Signals refer to the SOR-422U specification. D12-D15 are sign-extended from D11, per channel. The /RDY signal is not shown here and is always low for all channels.

XILINX ZC706 NET NAME	VITA STANDARD FMC SIGNAL NAME	FMC ROW	FMC PIN	DM CHANNEL	DM SIGNAL
FMC_xPC_LA14_P	LA14_P	C	18	0	D11
FMC_xPC_LA10_N	LA10_N	C	15	0	D10
FMC_xPC_LA10_P	LA10_P	C	14	0	D9
FMC_xPC_LA06_N	LA06_N	C	11	0	D8
FMC_xPC_LA06_P	LA06_P	C	10	0	D7
FMC_xPC_LA13_N	LA13_N	D	18	0	D6
FMC_xPC_LA13_P	LA13_P	D	17	0	D5
FMC_xPC_LA05_N	LA05_N	D	12	0	D4
FMC_xPC_LA02_N	LA02_N	H	8	0	D3
FMC_xPC_LA02_P	LA02_P	H	7	0	D2
FMC_xPC_LA09_P	LA09_P	D	14	0	D1
FMC_xPC_LA09_N	LA09_N	D	15	TX LED/PIN	
FMC_xPC_LA05_P	LA05_P	D	11	0	D0
FMC_xPC_LA00_CC_P	LA00_CC_P	G	6	0	/CLK
FMC_xPC_LA03_P	LA03_P	G	9	0	/ENA
FMC_xPC_LA04_P	LA04_P	H	10	0	/FRM
FMC_xPC_LA03_N	LA03_N	G	10	1	D11
FMC_xPC_LA04_N	LA04_N	H	11	1	D10
FMC_xPC_LA08_P	LA08_P	G	12	1	D9
FMC_xPC_LA08_N	LA08_N	G	13	1	D8
FMC_xPC_LA07_P	LA07_P	H	13	1	D7
FMC_xPC_LA07_N	LA07_N	H	14	1	D6
FMC_xPC_LA12_P	LA12_P	G	15	1	D5
FMC_xPC_LA12_N	LA12_N	G	16	1	D4
FMC_xPC_LA16_N	LA16_N	G	19	1	D3
FMC_xPC_LA11_N	LA11_N	H	17	1	D2
FMC_xPC_LA16_P	LA16_P	G	18	1	D1
FMC_xPC_LA11_P	LA11_P	H	16	1	D0
FMC_xPC_LA01_CC_P	LA01_CC_P	D	8	1	/CLK
FMC_xPC_LA15_P	LA15_P	H	19	1	/ENA
FMC_xPC_LA20_N	LA20_N	G	22	1	/FRM

XILINX ZC706 NET NAME	VITA STANDARD FMC SIGNAL NAME	FMC ROW	FMC PIN	DM CHANNEL	DM SIGNAL
FMC_xPC_LA20_P	LA20_P	G	21	2	D11
FMC_xPC_LA15_N	LA15_N	H	20	2	D10
FMC_xPC_LA23_P	LA23_P	D	23	2	D9
FMC_xPC_LA23_N	LA23_N	D	24	2	D8
FMC_xPC_LA19_P	LA19_P	H	22	2	D7
FMC_xPC_LA19_N	LA19_N	H	23	2	D6
FMC_xPC_LA22_P	LA22_P	G	24	2	D5
FMC_xPC_LA22_N	LA22_N	G	25	2	D4
FMC_xPC_LA25_P	LA25_P	G	27	2	D3
FMC_xPC_LA27_P	LA27_P	C	26	2	D2

FMC_xPC_LA21_P	LA21_P	H	25	2	D1
FMC_xPC_LA21_N	LA21_N	H	26	2	D0
FMC_xPC_LA18_CC_P	LA18_CC_P	C	22	2	/CLK
FMC_xPC_LA26_P	LA26_P	D	26	2	/ENA
FMC_xPC_LA24_P	LA24_P	H	28	2	/FRM
FMC_xPC_LA26_N	LA26_N	D	27	3	D11
FMC_xPC_LA29_P	LA29_P	G	30	3	D10
FMC_xPC_LA24_N	LA24_N	H	29	3	D9
FMC_xPC_LA28_P	LA28_P	H	31	3	D8
FMC_xPC_LA28_N	LA28_N	H	32	3	D7
FMC_xPC_LA29_N	LA29_N	G	31	3	D6
FMC_xPC_LA31_P	LA31_P	G	33	3	D5
FMC_xPC_LA30_P	LA30_P	H	34	3	D4
FMC_xPC_LA32_N	LA32_N	H	38	3	D3
FMC_xPC_LA32_P	LA32_P	H	37	3	D2
FMC_xPC_LA30_N	LA30_N	H	35	3	D1
FMC_xPC_LA31_N	LA31_N	G	34	3	D0
FMC_xPC_LA17_CC_P	LA17_CC_P	D	20	3	/CLK
FMC_xPC_LA33_N	LA33_N	G	37	3	/ENA
FMC_xPC_LA33_P	LA33_P	G	36	3	/FRM

XILINX ZC706 NET NAME	VITA STANDARD FMC SIGNAL NAME	FMC ROW	FMC PIN	DAC SIGNAL
FMC_xPC_LA20_P	LA20_P	G	21	SDI
FMC_xPC_LA15_N	LA15_N	H	20	SCK
FMC_xPC_LA14_N	LA14_N	C	19	/CS